

Form PTO-1449 (modified)

Atty. Docket No.
SILA:095Serial No.
10/081,121

List of Patents and Publications for Applicant's

Applicants
JEFFREY W. SCOTT ET AL.

RECEIVED

DEC 04 2002

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Filing Date:
2/22/02Group:
2682

Technology Center 2800

U.S. Patent Documents
See Pages 1-3Foreign Patent Documents
See Page 3Other Art
See Pages 3-10

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
00	C87	Motorola Communications Semiconductor Product Division, "A 1.9 GHz Chipset For PCS Applications," Microwave Journal, No. 6, June 1995, 3 pgs.
	C88	Search Report for PCT/US02/00896; October 4, 2002; 7 pgs.
	C89	Copending U.S. Patent Application Serial No. 09/708,339, filed November 8, 2000, "Method And Apparatus For Operating A PLL With A Phase Detector/Sample Hold Circuit For Synthesizing High-Frequency Signals For Wireless Communications" (Sila:035C1)
	C90	Copending U.S. Patent Application Serial No. 09/999,702, filed October 31, 2001, "Method And Apparatus For Synthesizing Dual Band High-Frequency Signals For Wireless Communications" (Sila:060C1)
00	C91	Search Report for PCT/US02/00895; November 11, 2002; 6 pgs.

Examiner:

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Date Considered:

5/19/05 ml 9/6/06

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Information Disclosure Statement — PTO-1449 (Modified)